

Fig. 1

Fig. 2

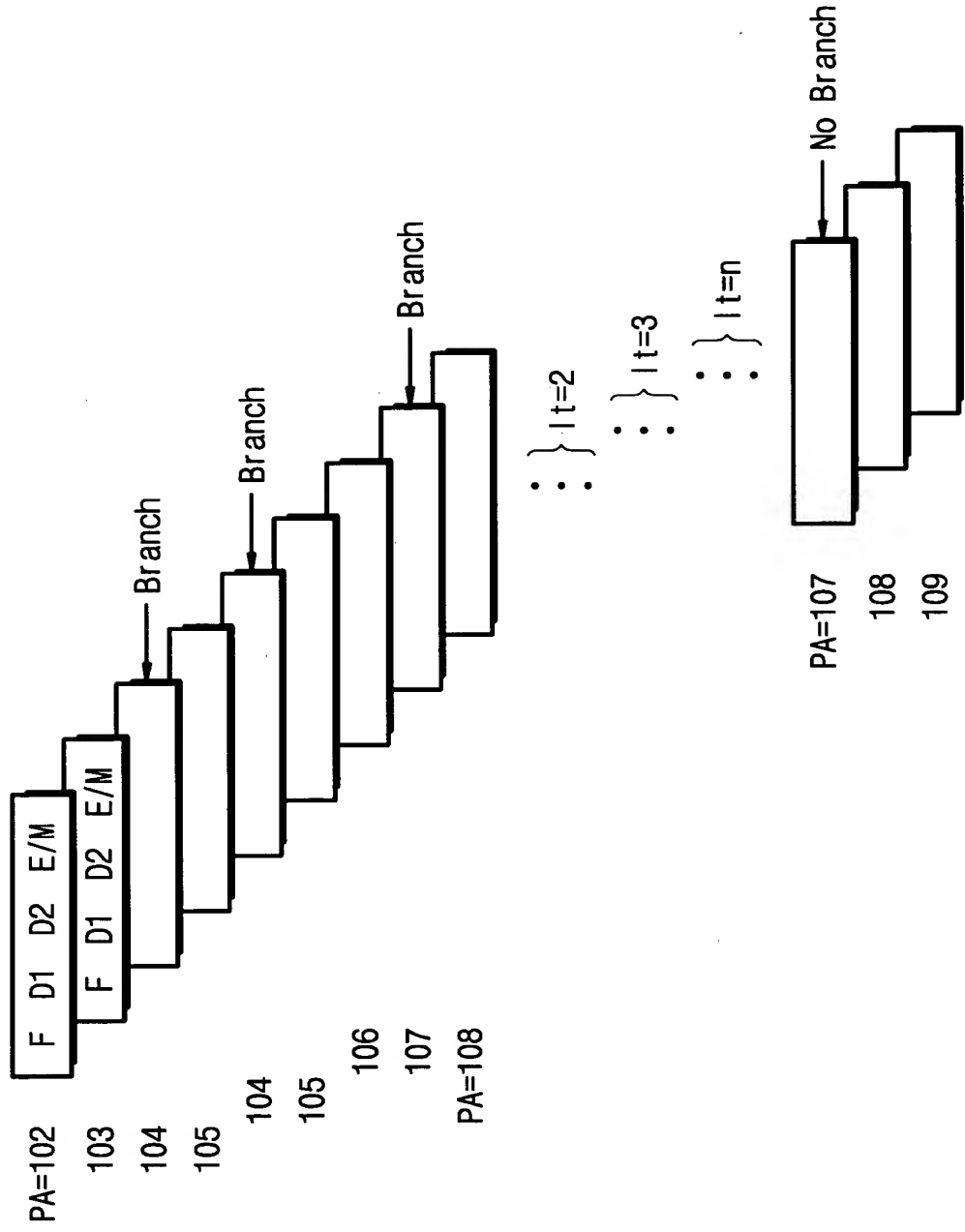


Fig. 3

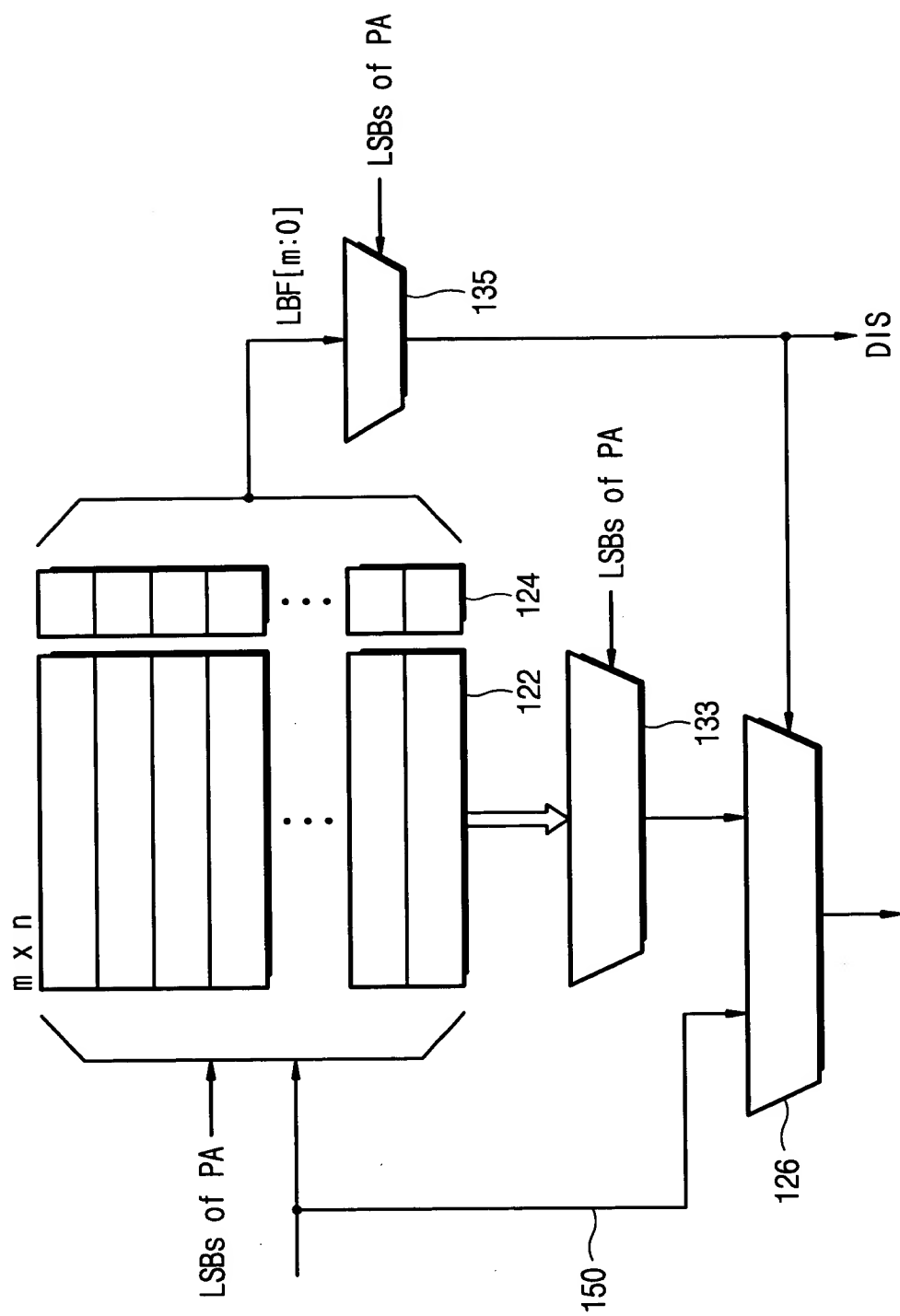


Fig. 4

Inst Cycle	Loop Ptr	PA	LBR[8:0]	PD	Buffer Fill	Branch Force	Nested Loop
1	2	102	00000000	PM	Fi		
2	3	103	000000100	PM	Fi		
3	4	104	000001100	PM	Fi	branch #dist1	
4	5	105	000011100	PM	Fi		nest1
5	4	104	000111100	LB		branch #dist1	
6	5	105	000111100	LB			nest1
7	6	106	000111100	PM	Fi		
8	7	107	001111100	PM	Fi	branch #dist2	
9	8	108	011111100	PM	Fi		Long Loop
10	2	102	111111100	LB			
11	3	103	111111100	LB			
12	4	104	111111100	LB		branch #dist1	
13	5	105	111111100	LB			
14	4	104	111111100	LB		branch #dist1	
15	5	105	111111100	LB			
16	6	106	111111100	LB			
17	7	107	111111100	LB		branch #dist2	
18	8	108	111111100	LB			
19	9	109	00000000	PM			
20	10	110	00000000	PM			